

PATENT ABSTRACTS OF JAPAN

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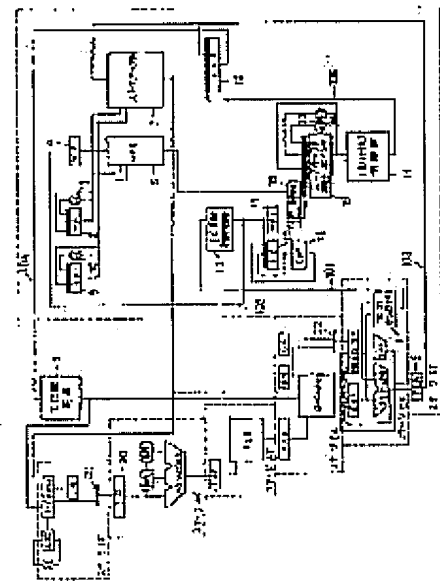
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(54) SWEEPING-OUT CONTROL SYSTEM

(57)Abstract:

PURPOSE: To attain the control to delay the re-writing of a main storage device up to the completion of an instruction by adding an instruction completion flag to an instruction unit for a storing request issued in the storing instruction.

CONSTITUTION: At present, the content of a sweeping-out pointer 19 is 1 and the content of an arithmetic result concerning the instruction A of a storing buffer 7 is swept out to the main storage device 8 by a memory access control part 11. At this time, for the content of the arithmetic result concerning an instruction B, when a storing action to the buffer 7 is started, 1 is stored into an instruction completion flag buffer 6 and the value of a buffer sweeping-out points 19 is updated to 2. When the sweeping-out to the main storage device 8 of the instruction A is completed, 1 from the buffer 6 is detected, the value of the pointer 19 is 1 and the content of the arithmetic result concerning the instruction B is stored into the main storage device 8.



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